



## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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<p>(21) International Application Number: <b>PCT/CA95/00460</b> (22) International Filing Date: 31 July 1995 (31.07.95)</p> <p>(71) Applicants (for all designated States except US): LITTON SYSTEMS CANADA LIMITED [CA/CA]; 28 City View Drive, Etobicoke, Ontario M9W 5A7 (CA). SUNNY-BROOK HOSPITAL [CA/CA]; 2075 Bayview Avenue, North York, Ontario M4N 3M5 (CA).</p> <p>(72) Inventors; and (75) Inventors/Applicants (for US only): WAECHTER, David [CA/CA]; Apartment 1704, 210 Steeles Avenue West, Brampton, Ontario L6Y 2K3 (CA). ZHAO, Wei [CA/CA]; Apartment 1624, 30 Charles Street West, Toronto, Ontario M4Y 1R5 (CA). ROWLANDS, John [CA/CA]; 47 Hanley Street, Toronto, Ontario M6S 2H3 (CA). HUANG, Zhong, Shou [CA/CA]; 1485 Gulliden Drive #28, Mississauga, Ontario L4X 2T2 (CA).</p> <p>(74) Agents: PERRY, Stephen, J. et al.; Sim &amp; McBurney, Suite 701, 330 University Avenue, Toronto, Ontario M5G 1R7 (CA).</p>		<p>(81) Designated States: CA, JP, US, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).</p> <p>Published With international search report.</p>
<p>(54) Title: <b>FLAT PANEL DETECTOR FOR RADIATION IMAGING WITH REDUCED TRAPPED CHARGES</b></p> <div data-bbox="293 1119 1304 1629"> </div> <p>(57) Abstract</p> <p>A flat panel detector (20) for radiation imaging includes an array of transistor switches (38) each of which is associated with a pixel electrode (36). A radiation transducer (CSE) including a top electrode (72) and a radiation conversion layer (70) is disposed over the array. Means are positioned over dead zones between adjacent pixel electrodes to inhibit the accumulation of charge in the radiation conversion layer at the dead zones when the top electrode is biased and the flat panel detector is exposed to radiation. In one embodiment, the inhibiting means is constituted by islands (64) formed of semiconductor material between the array and the radiation transducer. Each island is positioned over a dead zone between adjacent pixel electrodes and contacts a pixel electrode to allow charges accumulated on the islands to drift to the pixel electrodes.</p>		

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FLAT PANEL DETECTOR FOR RADIATION IMAGING  
WITH REDUCED TRAPPED CHARGES

TECHNICAL FIELD

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The present invention relates to imaging systems and in particular to a flat panel detector for radiation imaging.

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BACKGROUND ART

X-ray imaging systems are widely used in medical diagnosis and industrial and security inspection environments. One well known prior art x-ray imaging system is commonly referred to as an x-ray image intensifier ("XII") system. The XII system includes a large image tube that converts a low intensity x-ray image into a visible image. Incident x-rays are transmitted through a low absorbing window, then absorbed by an input phosphor screen and converted into a light image. On the inner surface of the input phosphor screen is a photocathode which converts the light into photoelectrons. The photoelectrons are accelerated and focused by an electrical static lens. The focused photoelectrons bombard an output phosphor screen and are converted into an optical image. A charge-coupled device ("CCD") or a camera tube is coupled to the output phosphor screen to convert the light image into an electronic video signal.

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However, the XII system suffers from a number of problems due to the multiple conversion stages, resulting in a reduction in image resolution and image contrast as well as pincussion distortion caused by the magnification error of the electrical static lens. Moreover, the XII system is complex and bulky.

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To overcome the problems associated with the XII system, alternative x-ray imaging systems employing

flat-panel radiation image sensors have been proposed. For example, U.S. Patent No. 4,382,187 to Fraleux et al. and U.S. Patent No. 4,689,487 to Nishiki et al. disclose early designs of large area flat-panel radiation image sensors for use in radiation imaging systems. These flat-panel sensors are responsive to incident x-rays and generate output signals representative of a radiation image.

U.S. Patent No. 5,079,426 to Antonuk et al. discloses a direct-detection x-ray image sensor incorporating an amorphous silicon thin film transistor ("TFT") switch and photodiode array. X-rays are detected by a phosphor screen that is placed on the top of the TFT switch and photodiode array. When x-rays interact with the phosphor film, light photons are generated and converted into electronic charges by the photodiode array. The charges are read out via the TFT switches to generate an image. However, problems exist with this sensor. Because the sensor employs a phosphor screen to detect the x-rays, blurring occurs due to the fact that the light photons are emitted in all directions and are scattered inside the phosphor screen. This results in a poor image resolution.

In an article entitled "New solid-state image pickup devices using photosensitive chalcogenide glass film," by T. Tsukada et al., published in the Proceedings of IEEE International Electron Devices Meeting, 1979, pp.134-136, a solid state image sensor is disclosed including a photoconductive selenium film deposited on a n-channel MOSFET switch array made from crystalline silicon. Although this image sensor is suitable for some imaging applications, it is not suited for large area radiation imaging applications due to the difficulties in fabricating a large sensor array on a crystalline silicon wafer.

In an article entitled "Digital radiology using self-scanned readout of amorphous selenium," authored by W. Zhao et al. presented at COMP91, Canadian Organization of Medical Physicists, Winnipeg, Manitoba, Canada, June 19, 1991, a flat-panel x-ray image sensor is disclosed. The image sensor includes a thick amorphous selenium film (a-Se) on a two-dimensional TFT switch array. The TFT switches are arranged in rows and columns to form a two-dimensional imaging system. Gate lines interconnect the TFT switches in each row while source lines interconnect the TFT switches in each column. The thick selenium film is deposited directly on top of the TFT switch array and a top electrode is deposited on the selenium film. When x-rays are incident on the selenium film and the top electrode is biased with a high voltage, electron-hole pairs are generated and separated by the electric field across the thickness of the selenium film. The holes which are driven by the electric field move toward the pixel electrodes (i.e. the drain electrodes of the TFT switches) and accumulate. This results in a charge being held by the pixel electrodes which can be used to develop an x-ray image. The charge held by the pixel electrodes can be read by supplying a pulse to each gate line. When a gate line receives a pulse, the TFT switches in the row turn on, allowing the signal charges on the pixel electrodes to flow to the source lines. Charge amplifiers connected to the source lines sense the charge and provide output voltage signals proportional to the charge and hence, proportional to the radiation exposure on the selenium film.

Because a thick amorphous selenium film is deposited on the TFT switch array, a few problems may arise which reduce image quality. During X-ray exposure, most of the holes are drawn to the pixel electrodes by the applied electric field, but some of them are drawn to the dielectric film which overlies the source and gate

lines. As this occurs, the electric field above the dielectric film decreases. Because the quantum efficiency of an a-Se film is approximately proportional to the electric field in the bulk of the a-Se film, signal charges generated by x-ray exposure of the a-Se film decline. Once the electric field decreases to a certain level, x-ray generated holes become trapped in the bulk of the selenium film above the dielectric film. Also, the trapped holes in the bulk of the selenium film may be released slowly by thermal energy and collected by adjacent pixel electrodes, resulting in decay-lag which again affects image quality.

European Patent No. 0,588,397 discloses an x-ray imaging device designed to deal with the above described problems. The x-ray imaging device includes a doped semiconductor layer covering all of the TFT switch array with the exception of the pixel electrodes. The doped semiconductor film allows holes collected in the semiconductor film above the source and gate lines (i.e. the area between adjacent pixel electrodes) to drift towards the pixel electrodes. However, one problem exists in that since the semiconductor layer overlays the entire area of the TFT switch array between the pixel electrodes, a diffusion of charges from one pixel area to adjacent pixel areas may occur especially around bright image locations. This results in a reduction in image resolution. Accordingly, there is a need for an improved flat panel detector for radiation imaging.

It is therefore an object of the present invention to provide a novel flat panel detector for radiation imaging which obviates or mitigates at least one of the above-mentioned problems.

DISCLOSURE OF THE INVENTION

According to one aspect of the present invention there is provided a flat panel detector for radiation imaging comprising:

an array of pixels each of which includes a pixel electrode to store a charge proportional to the exposure of said pixel to radiation;

a radiation transducer over said array to be exposed to incident radiation; and

means positioned over dead zones between adjacent pixel electrodes to inhibit the accumulation of charge in said radiation transducer over said dead zones.

According to another aspect of the present invention there is provided a flat panel detector for radiation imaging comprising:

a radiation transducer including a radiation conversion layer and an electrode on one side of said radiation conversion layer;

an array of pixels arranged in rows and columns on the other side of said radiation conversion layer, each of said pixels including a pixel electrode to accumulate charge as a result of hole drift in said radiation conversion layer occurring upon exposure of said radiation transducer to radiation and when said electrode is biased;

a plurality of source lines upon which charges accumulated by said pixels can be sensed, each of said source lines connecting the pixels in one of said rows or columns;

a plurality of gate lines upon which gating signals are supplied to allow accumulated charges to be sensed, each of said gate lines connecting the pixels in the other of said rows or columns; and

means to inhibit the accumulation of charge in said radiation transducer over dead zones between adjacent pixel electrodes.

5                   In still yet another aspect of the present invention there is provided in a method of forming a flat panel detector for radiation imaging including a radiation transducer having a radiation conversion layer and an electrode on one side thereof and an array of  
10 pixels arranged in rows and columns on the other side of said radiation conversion layer, each pixel including a pixel electrode to store charge proportional to the exposure of said flat panel detector to radiation, said method further comprising the step of:  
15                   providing means over dead zones between adjacent pixel electrodes to inhibit the accumulation of charge in said radiation transducer.

20                   In one embodiment, the means to inhibit is in the form of semiconductor material film islands at the dead zones between adjacent pixel electrodes which allow charges trapped therein to migrate towards a pixel electrode. In another embodiment, it is preferred that the means to inhibit is in the form of a patterned  
25 electrode forming part of the radiation transducer so that no electrode exists at the dead zones. In yet another embodiment, it is preferred that the means to inhibit is in the form of an insulator between the electrode and the radiation conversion layer at the dead  
30 zones.

By preventing charges from being trapped in the radiation conversion layer and inhibiting charges from migrating to a plurality of adjacent pixel sensors, the  
35 flat panel detector allows for the creation of higher resolution radiation images.



**BRIEF DESCRIPTION OF THE DRAWINGS**

Embodiments of the present invention will now be described more fully with reference to the accompanying drawings in which:

Figure 1 is a schematic of a flat panel detector for radiation imaging;

Figure 2 is an equivalent circuit of a pixel forming part of the flat panel detector illustrated in Figure 1;

Figure 3 is a top plan view of a portion of the flat panel detector of Figure 1;

Figure 4 is a cross-sectional view of Figure 2 taken along line 4-4;

Figure 5 is a cross-sectional view of Figure 2 taken along line 5-5;

Figure 6 is a cross-sectional view of a portion of an alternative embodiment of a flat panel detector for radiation imaging;

Figure 7 is a top plan view of a portion of yet another embodiment of a flat panel detector for radiation imaging;

Figure 8 is a cross-sectional view of Figure 7 taken along line 8-8;

Figure 9 is a cross-sectional view of a portion of still yet another embodiment of a flat panel detector for radiation imaging; and

Figures 10a, 10b and 10c are cross-sectional views showing the process by which the portion of the flat panel detector of Figure 9 is created.

**BEST MODES FOR CARRYING OUT THE INVENTION**

Referring now to Figure 1, a flat panel detector for radiation imaging is shown and is generally indicated by reference numeral 20. The flat panel detector includes a plurality of pixels 22 arranged in

rows and columns. Gate lines 24 interconnect the pixels 22 of each row while source lines 26 interconnect the pixels 22 of each column. The gate lines 24 lead to a gate driver circuit 28 which provides pulses to the gate lines in succession in response to input from a control circuit 29. The source lines 26 lead to charge amplifiers 30 which in turn are connected to an analog multiplexer 32. The analog multiplexer provides image output which can be digitized to create a digitized radiation image in response to input from the control circuit 29.

Figure 2 shows an equivalent circuit of one of the pixels 22. As can be seen, the pixel includes a radiation transducer  $C_{SE}$  coupled to a storage capacitor  $C_{ST}$  in the form of a pixel electrode 36. The pixel electrode 36 constitutes the drain electrode of a thin film transistor ("TFT") switch 38. The source electrode of TFT switch 38 is coupled to one of the source lines 26 while the gate electrode of the TFT switch 38 is coupled to one of the gate lines 24.

When the radiation transducer  $C_{SE}$  is biased and is exposed to radiation, it causes the pixel electrode 36 to store a charge proportional to the exposure of the radiation transducer  $C_{SE}$  to radiation. Once charged, the charge can be read by supplying a gating pulse to the gate terminal of TFT switch 38. When the TFT switch receives the gate pulse, it connects the pixel electrode 36 to the source line 26 allowing the pixel electrode to discharge. The charge on the source line 26 is detected by the charge amplifier 30 which in turn generates an output voltage proportional to the detected charge. The output voltage of the charge amplifier 30 is conveyed to the analog multiplexer 32.

The design of the pixels 22 is such to obviate or mitigate at least some of the disadvantages associated with the prior art designs discussed previously by reducing the occurrence of charges being trapped in the radiation conversion layer in the dead zones of the flat panel detector between adjacent pixel electrodes 36.

Referring now to Figures 3, 4 and 5, a portion of the array of pixels 22 formed in accordance with the present invention is shown. The pixels 22 including the gate and source lines 24 and 26 are fabricated on a common glass substrate 50. Deposited on the substrate 50 are the chromium gate lines 24. A gate insulating layer 52 formed of  $\text{SiO}_2$  or  $\text{SiN}_x$  overlies the substrate 50 and the gate lines 24.

Deposited on the gate insulating layer 52 above the gate lines 24 are semiconductor material layers formed of cadmium selenide ( $\text{CdSe}$ ) defining the channels 54 of each TFT switch 38. A passivation layer 56 in the form of an  $\text{SiO}_2$  layer overlies the gate insulating layer 52 and the channels 54. Vias 58 are provided in the passivation layer 56 to expose a portion of each channel 54. Contacting each channel 54 through one of the vias 58 and overlying a portion of the passivation layer 56 are the pixel electrodes 36 which define the drain electrodes of the TFT switches 38. Source electrodes 60 also contact each channel 54 through the other of the vias 58 and overlie a portion of the passivation layer 56.

A top passivation layer 62 in the form of a photoresist is patterned so that it only overlies the active areas of the TFT switches 38. Thus, the majority of the pixel electrode 36 associated with each TFT switch 38 remains exposed. Semiconductor material film islands 64 overlie most of the patterned top passivation layer

62. Each semiconductor film island 64 contacts the pixel electrode of an adjacent TFT switch 38.

5           Above the TFT switch array is the radiation  
transducer  $C_{SE}$ . The radiation transducer is in the form  
of a selenium (Se) radiation conversion layer 70 having a  
thickness of approximately  $300\mu\text{m}$  to  $500\mu\text{m}$ . Above the  
radiation conversion layer is a top electrode 72 formed  
of In, Al or Au. The top electrode 72 is coupled to a  
10   high potential voltage source 74 in the order of 3kV to  
provide the necessary bias to the radiation transducer  
 $C_{SE}$ .

15           In operation, the top electrode 72 is biased to  
a high potential by source 74 and the flat panel detector  
20 is exposed to radiation, resulting in an electric  
field being created in the radiation conversion layer 70  
which causes electrons to move towards the top electrode  
72 and holes to move towards the pixel electrodes 36.  
20   The majority of the holes drift to the exposed pixel  
electrodes where positive charges are accumulated.  
However, some holes drift to the semiconductor film  
islands 64 overlying the patterned top passivation layer  
62. Because each semiconductor film island 64 contacts  
25   only one pixel electrode 36, the charge accumulated by  
each semiconductor film island 64 drifts to an individual  
pixel electrode 36. Therefore, the provision of the  
semiconductor film islands 64 over the patterned top  
passivation layer 62 and contacting one pixel electrode  
30   36 reduces decay-lag resulting from trapped holes in the  
dead zones of the flat panel detector 20 between adjacent  
pixel electrodes 36 and inhibits the migration of charge  
from one pixel 22 to another.

35           During fabrication of the flat panel detector  
20, the passivation layer 62 and the semiconductor film  
64 are deposited on the TFT switch array as continuous

layers. Photolithography techniques are then used to pattern layer 62 and film 64.

Referring now to Figure 6, a pixel forming a portion of another embodiment of a flat panel detector 120 for radiation imaging is shown. In this embodiment, like reference numerals will be used to indicate like components with "100" added for clarity. Unlike the previous embodiment, each TFT switch 138 includes a bottom gate line 124 as well as a top gate electrode 90. The top gate electrodes 90 and the bottom gate lines 124 are electrically connected and are driven by a negative potential until it is desired to read the charges held by the pixel electrodes 136. The negative potential on the top gate electrodes 90 and the bottom gate lines 124 inhibits charges that accumulate in the semiconductor film islands 64 from turning the TFT switches 138 on while the flat panel detector 20 is exposed to incident radiation.

Referring now to Figures 7 and 8, yet another embodiment of a flat panel detector 220 for radiation imaging is shown. In this embodiment, like reference numerals will be used to indicate like components with a "200" added for clarity. As can be seen, the flat panel detector 220 is very similar to that shown in Figures 3 to 5. However, the passivation layer 62 and semiconductor film islands 64 have been removed and the top electrode 272 has been patterned so that no electrode exists over the dead zones of the flat panel detector 220. This absence of the top electrode 272 over the dead zones prevents an electric field from being created in the radiation conversion layer 270 in those zones and therefore, no electron-hole pairs are separated in those zones. This inhibits any charge from being trapped in the radiation conversion layer 270 over the dead zones. However, because the top electrode 272 remains over the

pixel electrodes 236, an electric field is created in the radiation conversion layer 270 over the pixel electrodes 236. This allows electron-hole pairs to be separated resulting in a charge being accumulated and stored by the pixel electrodes 236 when the flat panel detector is exposed to incident radiation.

Referring now to Figure 9, still yet another embodiment of a flat panel detector 320 for radiation imaging is shown. In this embodiment, like reference numerals will be used to indicate like components with a "300" added for clarity. The flat panel detector 320 is very similar to that shown in the previous embodiment with the exception that rather than removing portions of the top electrode 372 over the dead zones of the flat panel detector 320, an insulator 94 is deposited between the top electrode 372 and the radiation conversion layer 370 over the dead zones. The provision of the insulators 94 achieves the same results as does the previous embodiment by preventing an electric field from being created in the radiation conversion layer over the dead zones.

Figures 10a to 10c show how the insulators 94 can be deposited on the radiation conversion layer 370 before the top electrode 372 is placed on the radiation conversion layer. Specifically, prior to placing the top electrode 372 on the radiation conversion layer 370, the structure is subjected to a corona discharge to charge the top surface of the radiation conversion layer 370 to a high potential in the order of +3000 V as indicated by reference numeral 96. The TFT switches 338 are turned on and the substrate 350 is exposed to blue light 98. The blue light passes through the structure in all areas except at the area of the TFT switches 338 due to the opaque gate lines 324 and the area on the data lines (source lines). The light which passes through the

structure and into the radiation conversion layer 370 creates charge carriers in the radiation conversion layer which are drawn to its upper charged surface. The charge carriers neutralize the charge created by the corona discharge in the areas above the pixel electrodes 336. The structure is then sent through toner development. The remaining charged areas of the radiation conversion layer 370 (i.e. those regions over the active area of the TFT switches ("the dead zones")) are covered by toner particles 94 which act as a good insulator. The radiation conversion layer 370 and toner particles 94 are then coated with the top electrode 372.

The present invention provides advantages in that the design of the flat panel detector inhibits charges from being trapped in the dead zones of the TFT switch array (i.e. the areas between pixel electrodes). Also, since the pixels are isolated into islands, image blooming is reduced.

Although a number of embodiments of a flat panel detector for radiation imaging have been shown with particular reference to specific materials forming the various components, it should be apparent to those of skill in the art that other suitable materials may be used.

In addition, those of skill in the art will also appreciate that alternative structures to inhibit charges from being trapped in the radiation conversion layer or migrating to a plurality of adjacent pixel sensors are well within the scope of the present invention as defined by the appended claims.

WHAT IS CLAIMED IS:

1. A flat panel detector for radiation imaging comprising:

5 an array of pixels each of which includes a pixel electrode to store a charge proportional to the exposure of said pixel sensor to radiation;

a radiation transducer over said array to be exposed to incident radiation; and

10 means positioned over dead zones between adjacent pixel electrodes to inhibit the accumulation of charge in said radiation transducer over said dead zones.

2. A flat panel detector as defined in claim 1  
15 wherein said means to inhibit is constituted by islands formed of semiconductor material between said array and said radiation transducer, each island being positioned over a dead zone between adjacent pixel electrodes and contacting a pixel electrode.

20 3. A flat panel detector as defined in claim 2 wherein said each of said islands contacts only one pixel electrode.

25 4. A flat panel detector as defined in claim 3 wherein said islands are formed by laying a continuous semiconductor film over said array and etching said film to define said islands.

30 5. A flat panel detector as defined in claim 4 wherein said film is etched using photolithography techniques.

35 6. A flat panel detector as defined in claim 1 wherein said means to inhibit is constituted by a patterned top electrode forming part of said radiation



transducer, said top electrode being absent over said dead zones.

5           7. A flat panel detector as defined in claim 1  
wherein said radiation transducer is in the form of a top  
electrode and a radiation conversion layer below said top  
electrode and wherein said means to inhibit is  
constituted by insulation islands between said top  
electrode and said radiation conversion layer and  
10 positioned over said dead zones.

8. A flat panel detector as defined in claim 7  
wherein said insulation islands are formed of toner  
particles.

15

9. A flat panel detector as defined in claim 8  
wherein said insulation islands are formed by charging a  
surface of said radiation conversion layer over the dead  
zones and passing said radiation conversion layer through  
toner development prior to depositing the top electrode  
20 on said surface.

10. A flat panel detector as defined in claim 9  
wherein said radiation conversion layer is charged by  
25 subjecting said radiation conversion layer to a corona  
discharge to charge the entire surface and then  
subjecting the areas of said radiation conversion layer  
to be free of charge to blue light.

30           11. A flat panel detector for radiation imaging  
comprising:

a radiation transducer including a radiation  
conversion layer and an electrode on one side of said  
radiation conversion layer;

35           an array of pixels arranged in rows and columns  
on the other side of said radiation conversion layer,  
each of said pixels including a pixel electrode to

accumulate charge as a result of hole drift in said radiation conversion layer occurring upon exposure of said radiation transducer to radiation and when said electrode is biased;

5           a plurality of sources lines upon which charges accumulated by said pixels can be sensed, each of said source lines connecting the pixels in one of said rows or columns;

10           a plurality of gate lines upon which gating signals are supplied to allow accumulated charges to be sensed, each of said gate lines connecting the pixels in the other of said rows or columns; and

15           means to inhibit the accumulation of charge in said radiation transducer over dead zones between adjacent pixel electrodes.

12. A flat panel detector as defined in claim 11 wherein said means to inhibit is constituted by islands formed of semiconductor material between said array and said radiation transducer, each island being positioned over a dead zone between adjacent pixel electrodes and contacting a pixel electrode.

13. A flat panel detector as defined in claim 11 wherein said means to inhibit is constituted by a patterned top electrode forming part of said radiation transducer, said top electrode being absent over said dead zones.

14. A flat panel detector as defined in claim 11 wherein said means to inhibit is constituted by insulation islands between said top electrode and said radiation conversion layer and positioned over said dead zones.

15. In a method of forming a flat panel detector for radiation imaging including a radiation transducer

having a radiation conversion layer and an electrode on one side thereof and an array of pixels arranged in rows and columns on the other side of said radiation conversion layer, each pixel including a pixel electrode to store charge proportional to the exposure of said flat panel detector to radiation, said method further comprising the step of:

providing means over dead zones between adjacent pixel electrodes to inhibit the accumulation of charge in said radiation transducer.

16. The method of claim 15 wherein said inhibiting means is provided by forming a continuous semiconductor film between said array and said radiation conversion layer and etching said film to define islands positioned over said dead zones and contacting a pixel electrode.

17. The method of claim 15 wherein said inhibiting means is provided by patterning said electrode so that said electrode is absent over said dead zones.

18. The method of claim 15 wherein said inhibiting means is provided by forming insulation islands between said radiation conversion layer and said electrode over said dead zones.

19. The method of claim 18 wherein said insulation islands are formed by charging the surface of said radiation conversion layer over said dead zones and passing said radiation transducer through toner development prior to placing said electrode thereon.

20. The method of claim 19 wherein said radiation conversion layer is charged by subjecting said radiation conversion layer to a corona discharge to charge the one surface thereof and then subjecting the areas of said

radiation conversion layer to be free of charge to blue light.

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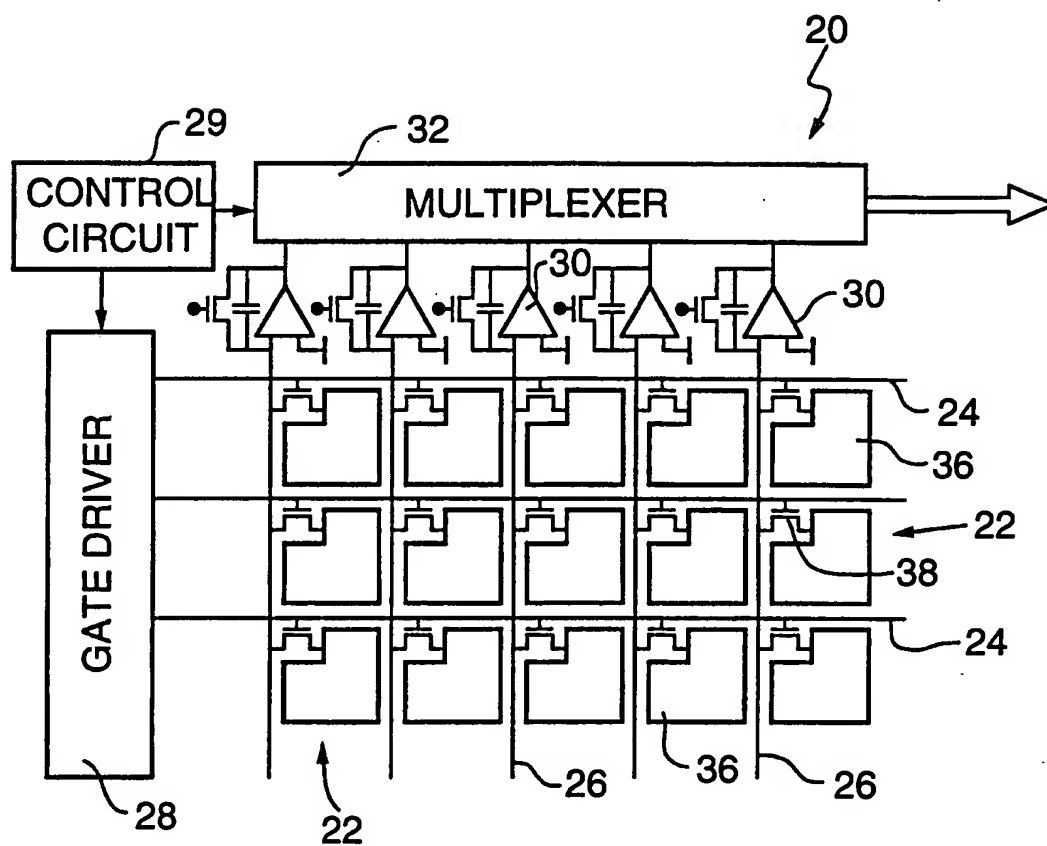


FIG.1

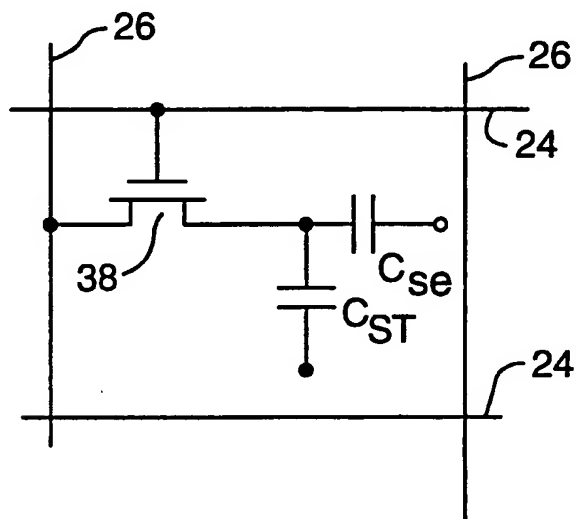


FIG.2

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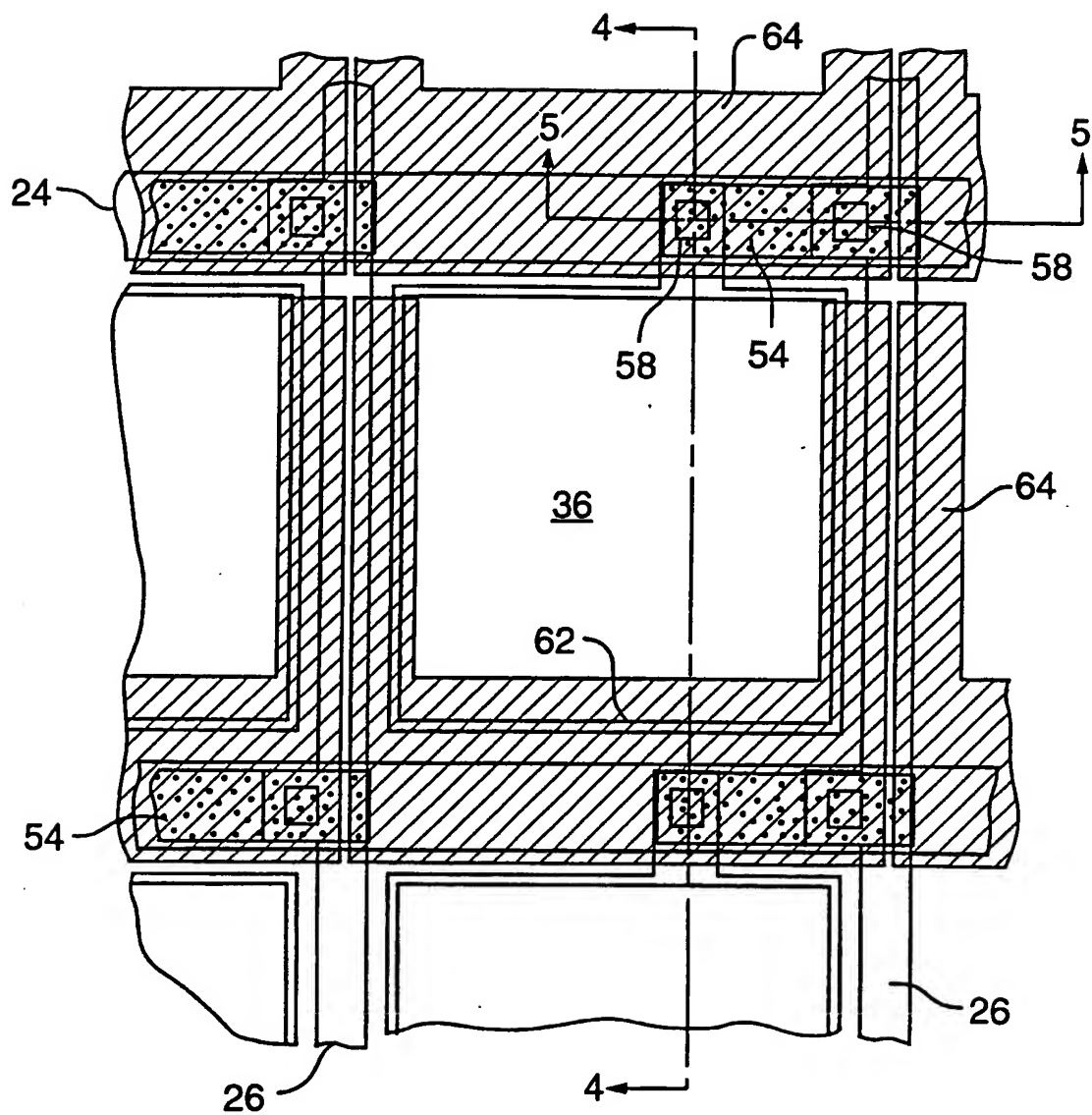


FIG.3

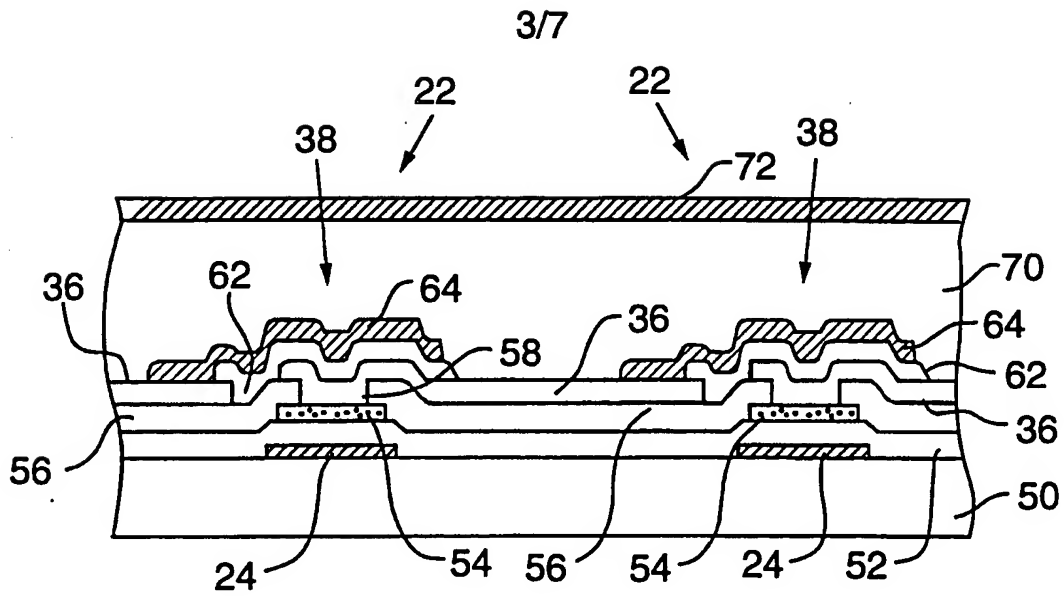


FIG. 4

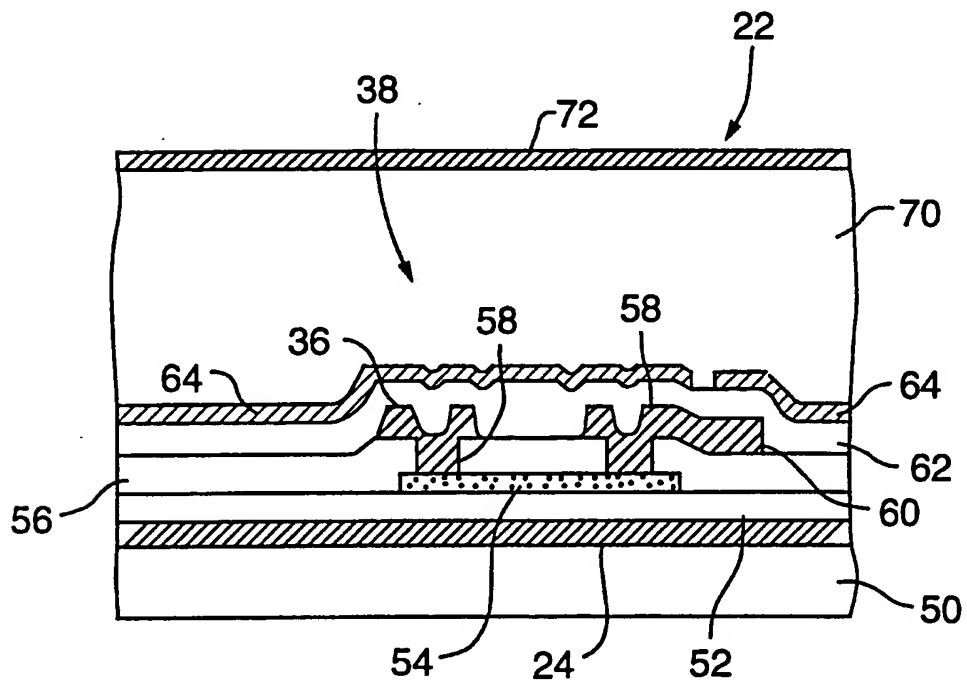


FIG. 5

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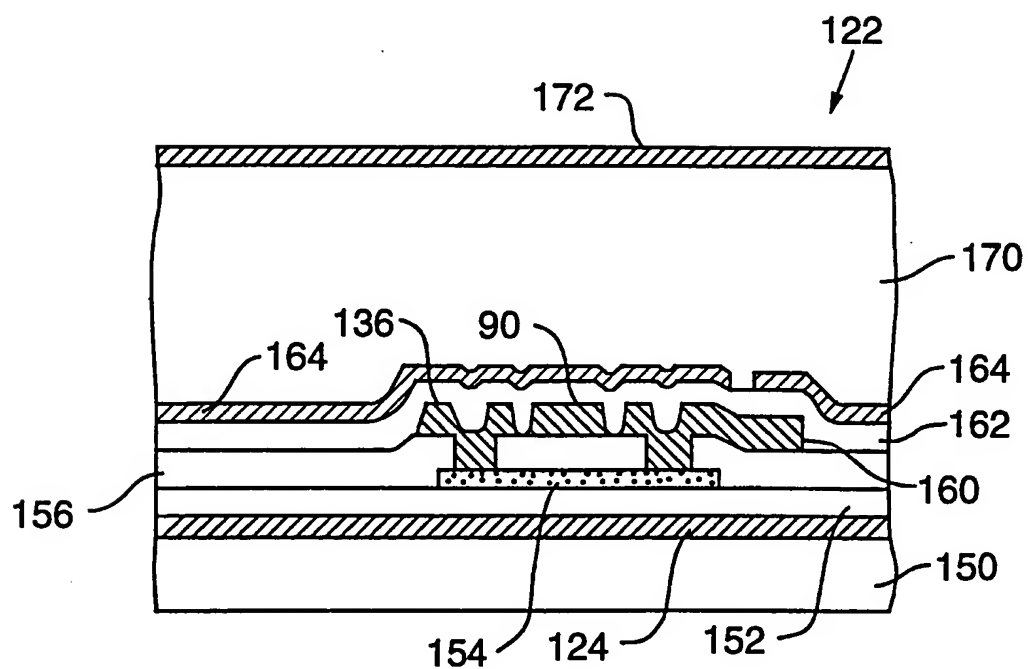


FIG. 6

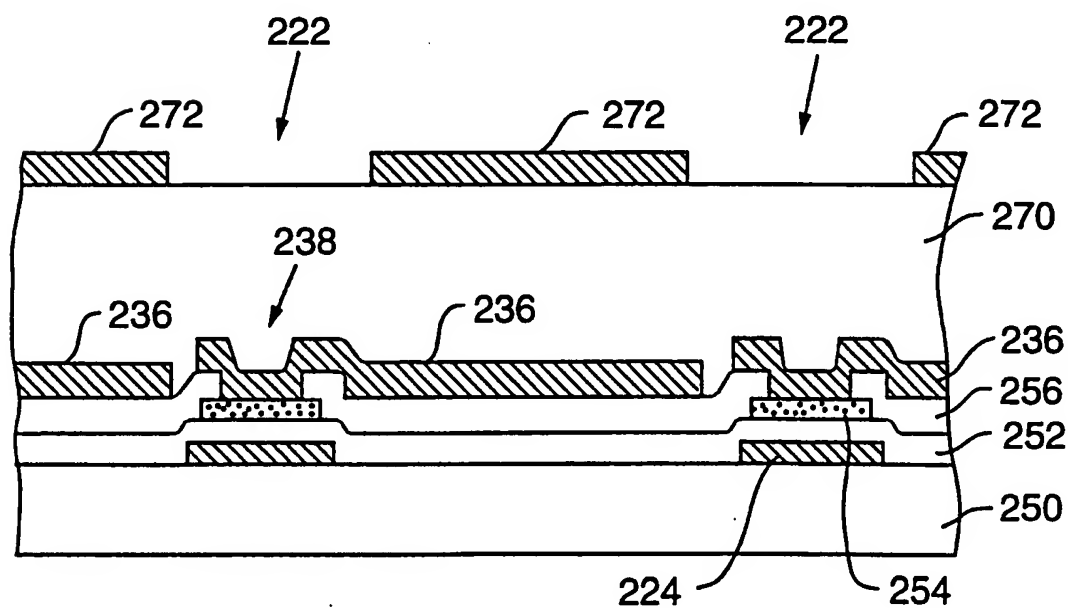


FIG. 8

SUBSTITUTE SHEET (RULE 26)



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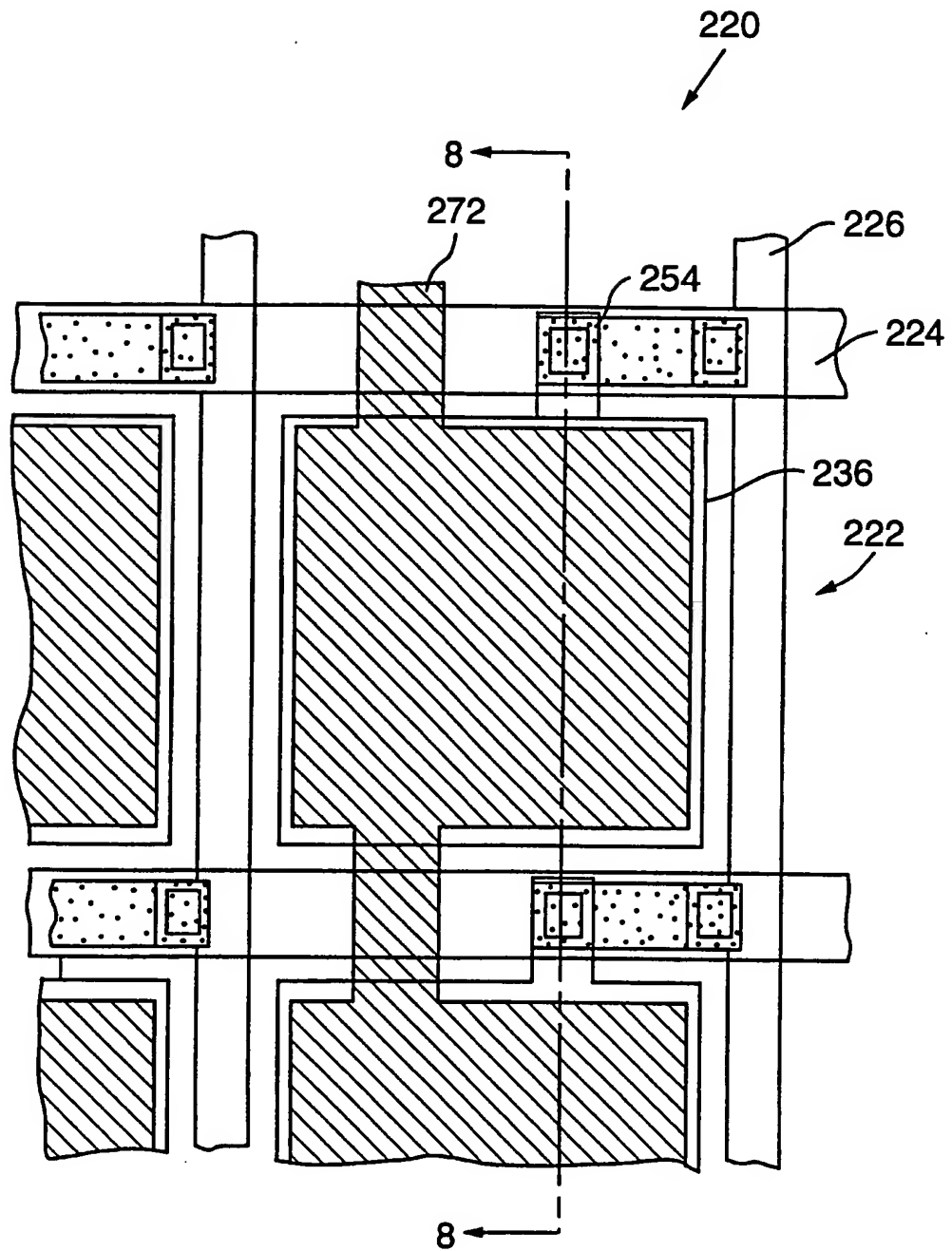


FIG. 7.

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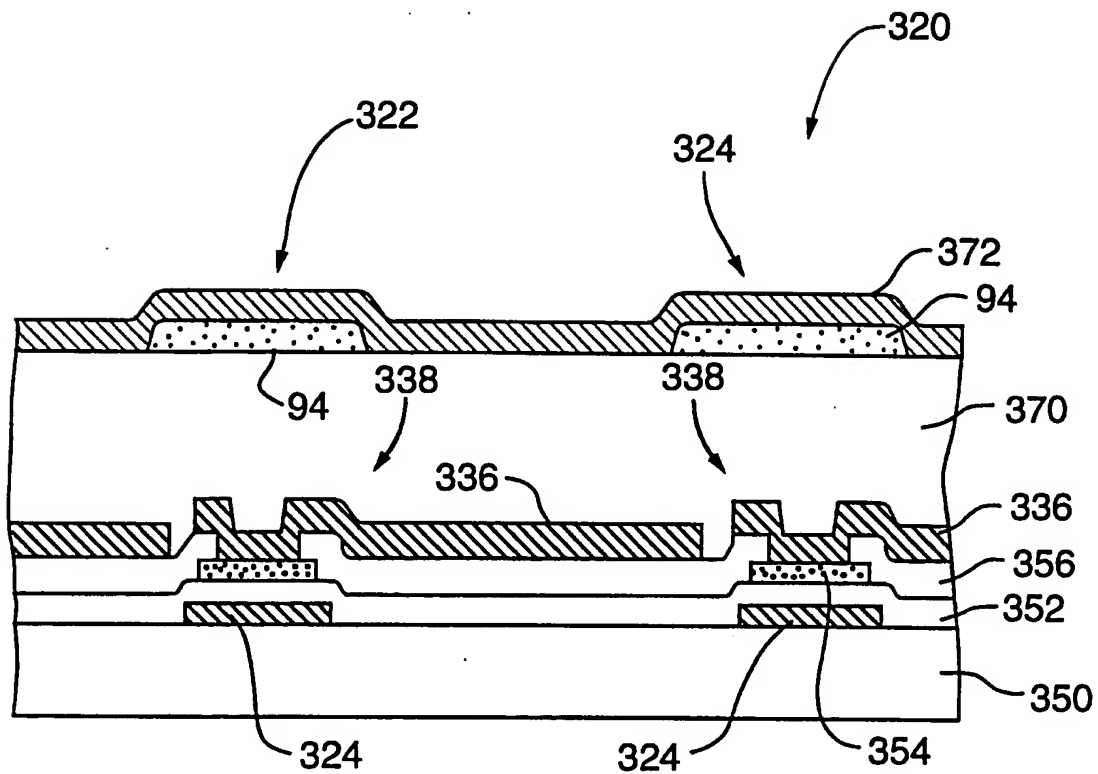


FIG. 9

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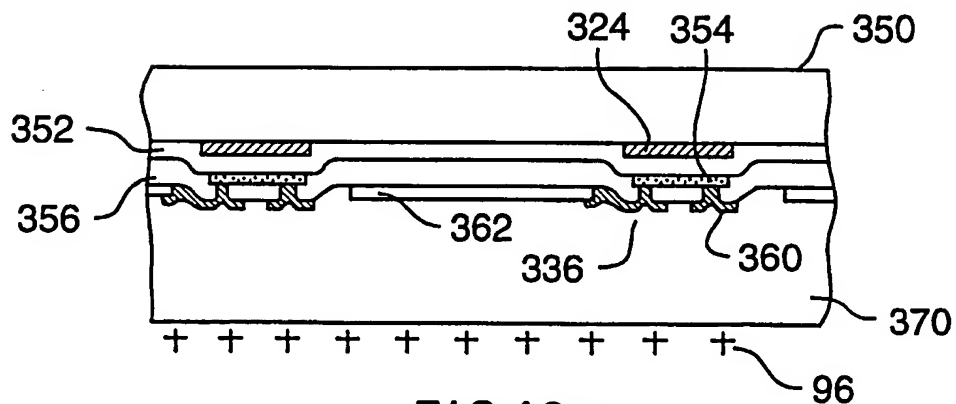


FIG. 10a

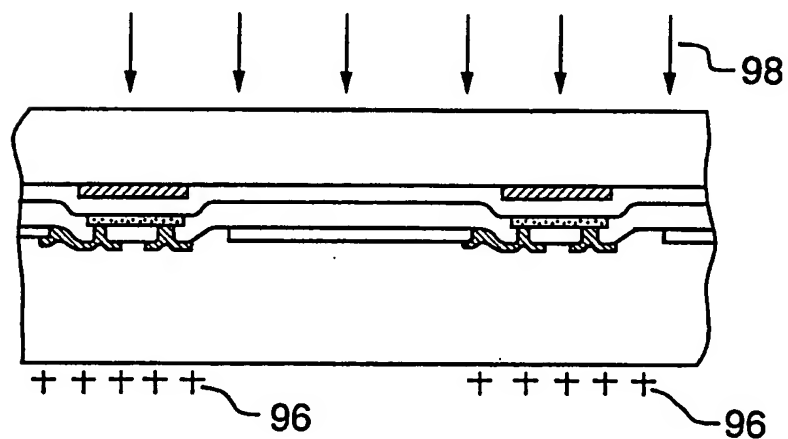


FIG. 10b

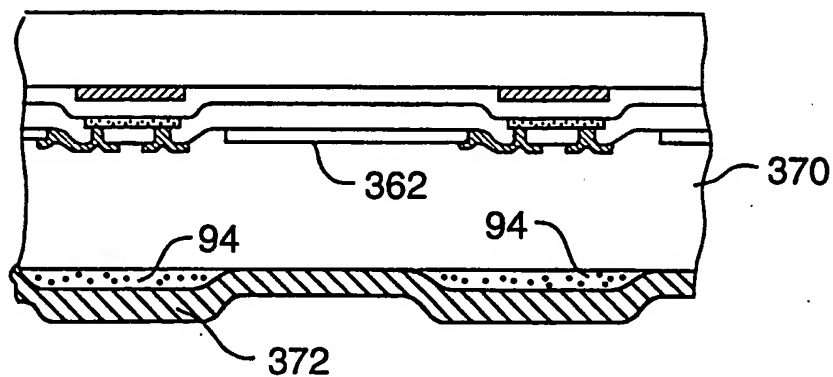


FIG. 10c

# INTERNATIONAL SEARCH REPORT

International Application No  
PCT/CA 95/00460

A. CLASSIFICATION OF SUBJECT MATTER  
IPC 6 H01L27/146

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
IPC 6 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	EP,A,0 588 397 (PHILIPS ELECTRONICS N.V.) 23 March 1994 cited in the application	1,11
A	see the whole document	2-10, 12-20
Y	--- IEEE TRANSACTIONS ON NUCLEAR SCIENCE, vol. 40, no. 1, February 1993 NEW YORK, USA, pages 40-44, XP 000304150 HIROSHI TSUTSUI ET AL. 'X-RAY ENERGY SEPARATION METHOD USING A CDTE SEMICONDUCTOR X-RAY IMAGING SENSOR AND PHOTON COUNTING METHOD' see figure 1 --- -/--	1,11

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

### \* Special categories of cited documents :

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- \* "O" document referring to an oral disclosure, use, exhibition or other means
- \* "P" document published prior to the international filing date but later than the priority date claimed

- \* "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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- \* "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- \* "&" document member of the same patent family

Date of the actual completion of the international search

8 March 1996

Date of mailing of the international search report

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Authorized officer

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# INTERNATIONAL SEARCH REPORT

International Application No  
PCT/CA 95/00460

C(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No. *
A	EP,A,0 574 690 (E.I. DU PONT DE NEMOURS AND COMPANY) 22 December 1993 see the whole document -----	1-20

# INTERNATIONAL SEARCH REPORT

Inte:      nal Application No  
PCT/CA 95/00460

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP-A-588397	23-03-94	DE-A- 4227096 JP-A- 6209097 US-A- 5396072	24-02-94 26-07-94 07-03-95
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EP-A-574690	22-12-93	EP-A- 0626592 JP-A- 6130196 US-A- 5313066	30-11-94 13-05-94 17-05-94
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